

IN THE CLAIMS:

1. (Cancelled).
- 5 2. (Previously Presented) The controller of claim 5, wherein said processor is integrated with said controller.
3. (Cancelled)
- 10 4. (Previously Presented) The controller of claim 5, wherein said at least one peripheral device employs said processor to perform each of said functions of said at least one peripheral device.
- 5 (Previously Presented) A controller for high speed communications between a host computer and at least one peripheral device, comprising:
15 a processor for controlling communications on a bus using one or more communication functions, wherein said processor performs at least one function for said peripheral device in addition to said one or more communication functions, wherein said processor provides processing capacity for use by said peripheral device, and wherein said high speed communications conform to a USB standard.
- 20 6. (Cancelled)
7. (Cancelled)
- 25 8. (Cancelled)
9. (Cancelled)
- 30 10. (Previously Presented) The method of claim 11, wherein said at least one peripheral device employs said first processor to perform each of said functions of said at least one peripheral

device.

11. (Currently Amended) A method for controlling communications between a host computer and at least one peripheral device, comprising the step of:

5 executing one or more communication functions that control communications on a bus using a first processor, wherein said first processor also performs at least one function for said peripheral device in addition to said one or more communication functions, wherein said processor provides processing capacity for use by said peripheral device, and wherein said high speed communications conform to a USB standard.

10 12 (Cancelled).

13. (Cancelled).

14. (Cancelled).

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15 (Previously Presented) The integrated circuit of claim 18, wherein said processor is integrated with said controller

16. (Cancelled)

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17 (Previously Presented) The integrated circuit of claim 18, wherein said at least one peripheral device employs said processor to perform each of said functions of said at least one peripheral device

25 18 (Currently Amended) An integrated circuit, comprising:

 a controller for high speed communications between a host computer and at least one peripheral device, comprising:

 a processor for controlling communications on a bus using one or more communication functions, wherein said processor performs at least one function for said
30 peripheral device in addition to said one or more communication functions, wherein said

processor provides processing capacity for use by said peripheral device, and wherein said high speed communications conform to a USB standard.

19. (Cancelled).

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